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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant

Kenji SHIGEKI, et al.

Serial No.

09/772,027

Filed

January 29, 2001

For

LOGIC INTEGRATED CIRCUIT, AND RECORDING MEDIUM READABLE BY A

COMPUTER, WHICH STORES A SOURCE OF CPU CORE ON SAID LOGIC INTEGRATED

CIRCUIT

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Group Art Unit

UNKNOWN

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INFORMATION DISCLOSURE STATEMENT

Sir:

Submitted herewith is an Information Disclosure Citation together with copies of the documents referred to therein. The degree of relevance of the

documents referred to in the Information Disclosure Citation is set forth in the Partial European Search Report also submitted herewith.

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Respectfully submitted,

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Bv

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conformance and not considered. Include copy of this form with next communication to applicant.

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Form PTO-1449 (Rev. 7-80) 42-44F (F-49)	U.S. Department of Commerce Patent and Trademark Office MATION DISCLOSURE CITATION (Use several sheets if necessary)	Atty. Docket No.: F-6810 Serial No.: 09/772,027 Applicant: Kenji SHIGEKI, et al.					
		Filing Date: Jar	Group:				
	U.S.	PATENT DOCUM	ENTS				
Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
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